

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination YONENAGA ET AL.	
		Examiner Danny Wai Lun Leung	Art Unit 2633	Page 1 of 1

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-5,543,952	08-1996	Yonenaga et al.	398/185
B	US-5,644,664	07-1997	Burns et al.	385/2
C	US-			
D	US-			
E	US-			
F	US-			
G	US-			
H	US-			
I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N					
O					
P					
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)	
U	Chapter 7, Introduction to CMOS design, by E. Di Zitti, WP3 Electronic Engineering Pilot University of Genoa (http://www.esng.dibe.unige.it/Projects/Netpro/PagineLocali/micro/doc/ch7i.PDF)	
V		
W		
X		

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.